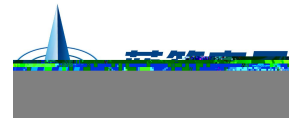


版本	修订日期	有修订的页码	修订涉及的内容	拟制	审核
A	2020-9-30				

BRCL3130CZF



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/ Descriptions

BRCL3130CZF / BRCL3130CZF
MOSFET
BRCL3130CZF DFN252-6L
BRCL3130CZF

6) # 7 89:;:(8<=# / 1> /# %13?@2*# >/# A#)>B)# >C* B1A*3C# /3@>3C# .31# D*)>@ 4 &D>3C,%3D E 4 1# 0A** 1E# %13* 2*3C!7 89:;:(8<=#23C*A>C/#A? AC2 ?#%3- 1# FG\$=H6I)>B)&A22@1A2E# 3D*AB #? * 2*3C#2>12@>*/# AC?#? DAE#2>12@>*/!#
7 89:;:(8<=#/ %@*#C*3#AC#@D*1A&/ 4 A0D#J=K '5' &L9#%A2MAB #AC?#3CDE#3C # N* 1CA0#234 %3C C*#4AM /# >*#AC#>? A0# /3D@>3C#>C#D>4* ?# /%A2 #3.#0A** 1E#%A2M!#7 89:;:(8<=#)A/#A0D#*) %13* 2*3C#.@C2*3C/ 1 0@>1 ?#>C#*) #0A** 1E#A%0D>2A*3C#>C2D@>?>CB#3 12)A1B>CBI#3 1?>/2)A1B>CBI#3 12@11 C*#AC?#D3A?#/)31* 2>12@>?>CB#%13* 2*3C# *2! 6) #A22@1A* #3 12)A1B>CB#? * 2*3C# 3D*AB # C/@1 /# /A. #AC?#.@D0#>D>PA*3C# 2)A1B>CB!6) #D3-#/*AC?0E#2@11 C*#?1A>C/#D*0 #2@11 C*#.134*) #2 D0#-)>D #>C#/*31AB ! 6) #? >2 #>/#C3* 3CDE#*A1B * ?#.31# ?>B*AD# 2 D0@D1A# %)3C /I# 0@*# AD/3# .31# ACE#3*) 1# 9>&Q3C# AC?# 9>&R3DE# 0A** 1E%3- 1 ?# >C.314A*3C#A%0D>AC2 /#1 0@>1>CB#D3CB& 14#0A** 1E#D>. !#

/ Features

- ◆# 45mΩ MOSFET
- ◆# DFN252-6L
- ◆#
- ◆# 0V
- ◆# 3.0uA <0.1uA;
- ◆#
- ◆#
- ◆# Q C* B1A* #A? AC2 ?#%3- 1# FG\$=H6#->*)#HO@> A0 C*#3.#ST4 U# J\$V GK
- ◆# YD*1A&/ 4 A0D#J=K '5' &L9#%A2MAB X#
- ◆# G 1&* 4% 1A*@1 #R13* 2*3C#G 12)A1B #8@11 C*#R13* 2*3C#G 1?>/2)A1B #8@11 C*#93A?#)\$)31* 8>12@>?>CB!#
- ◆# 8)A1B 1#? * 2*3C#.@C2*3C# (Z#0A** 1E#2)A1B>CB#.@C2*3C# ? DAE#*4 /#A1 #B C 1A* ?# >C/>? X [>B)&A22@1A2E# 3D*AB #? * 2*3C!#
- ◆# 93-#8@11 C*#83C/@ 4 %>3C#G% 1A*3C#F3? +#:!(\"#*E%X#R3- 1&?3-C#F3? +#] (!;@\"#*E%X#
- ◆# 6) #G 1?>/2)A1B #>/#C3*#/ D.#1 23 1E#AC?#C ?/#3#0 #A2* A* ?X#
- ◆# [A03B C&.1 #R13?@2*!#

/ Applications

GC &8 D0D#>)>@ 4 &3C#0A** 1E#%A2M#9*)>@ 4 &R3DE 4 1#0A** 1E#%A2M!

/ Typical Application



/ Pinning

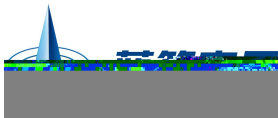
#
#

/ Marking

#

/ Absolute Maximum Ratings(Ta=25)

#



,RA1A4 * 1#			,6 /*#				
		,\$E4 030#	83C?>3C#	,F>C# #	,6E%#	,FAN#	,YC>#
G 12)A1B #J * 2*3C#Z30*AB #	Z8Y#	#		S!'T#	S!:(#	S!:T#	Z#
G 12)A1B # 0 A/ #Z30*AB #	Z89#	#		S!(T#	S!;(#	S!;T#	Z#
G 1?>/2)A1B #J * 2*3C#Z30*AB #	ZJ9#	#		'!e'#	'!b(#	'!bb#	Z#

/ Functionl Block Diagram



/ Functional Description

MOSFET

6) #7 89:;:(8<=#43C*31/*) # 30*AB #AC?#2@11 C*3.#A#0A** 1E#AC?#%13* 2*/#*#.134#0 >CB?#A4AB ?# ?@ #*3#3 12)A1B # 3D*AB I#3 1?>/2)A1B # 30*AB I#3 1?>/2)A1B #2@11 C*#AC?#/)31*#2>12@>*#23C?>*3C/# 0E# ?>/23CC 2*>CB*#) # 0A** 1E#.134#*) # D3A?#31#2)A1B 1!#6) # % 1%) 1A# 2>12@>*#>/# 1E# />4%# !#6) # FG\$=H6#>/#C* B1A* ?#AC?#*#/# J\$VGK#>/#A/#D3 - #A/#ST4 h#*E%>2A!#

#

/ Normal Operating mode

)**0%+,,- -!./01 2!234## # # # # # # # # # # # # # # # **4#,#12**

BRCL3130CZF

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#

/ Overcharge Condition

VCU ,

TCU

BRCL3130CZF

FET

1

VCL BRCL3130CZF

2

BRCL3130CZF

VM

0.7V

BRCL3130CZF

VCU

VCU BRCL3130CZF

VCU

VCU

VM

1

VCU

VCU

1

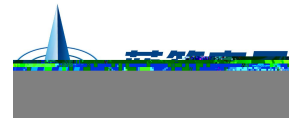
2

VCU

1

2

d) C#*) #0A** 1E# 30*AB #0 234 /#>)B) 1#*)AC#*) #3 12)A1B #? * 2*3C# 30*AB #VZ8YWH#?@1>CB#
2)A1B>CB#@C? 1#C314A0#23C?>*3C#AC?#*) #/*A* #23C*CB@ /#.31#*) #3 12)A1B #? * 2*3C#? DAE#*>4 #V68YWH
31#13CB 11#*) #7 89:;:(8<=#*@1C/#*) #2)A1B>CB#23C*130#=#H6#3..#*3# /*3%#2)A1B>CB!#6)>/#23C?>*3C#>/#
2A00 ?#*) #3 12)A1B #23C?>*3C!#
6) #3 12)A1B #23C?>*3C#>/#1 D A/ ?#C#*) #.3003->CB#*-3#2A/ /#
 ; #d) C#*) #0A** 1E# 30*AB #?13%/#0 D3-#*) #3 12)A1B #1 D A/ # 30*AB #VZ89YWH#*) #7 89:;:(8<=#
@1C/#) #2)A1B>CB#23C*130#=#H6#3C#AC?#1 *@1C/#*3#*) #C314A0#23C?>*3C!#
' #d) C#A#03A?#>/#23CC 2* ?#AC?#?>/2)A1B>CB#/*A1*/I#*) #7 89:;:(8<=#*@1C/#*) #2)A1B>CB#23C*130#
=#H6#3C#AC?#1 *@1C/#*3#*) #C314A0#23C?>*3C!#6) #1 D A/ #4 2)AC/>4#>/#A/#.3003-/#*) #?>/2)A1B>CB#
2@11 C*#.D3-/#*)13@B)#AC#>C* 1CA0#%A1A/>*2#?>?# 3.#*) #2)A1B>CB#=#H6#>44 ?>A* DE#A.* 1#A#D3A?#>/#
23CC 2* ?#AC?#?>/2)A1B>CB#/*A1*/I#AC?#*) #ZF#%>C# 30*AB #C21 A/ /#A03@#(!e#Z#V.31-A1?# 30*AB #3.#*) #
>?>? W#.134#*) #^KJ#%>C# 30*AB #434 C*A10EI#6) #7 89:;:(8<=#? * 2*/#*)>/# 30*AB #AC?#1 D A/ /#
) #3 12)A1B #23C?>*3C!#83C/ O@ C*0EI#>C#*) #2A/ #*)A#*) #0A** 1E# 30*AB #>/# O@A0#*3#1#D3-1#*)AC#
) #3 12)A1B #? * 2*3C# 30*AB #VZ8YWH#*) #7 89:;:(8<=# 1 *@1C/# *3#*) #C314A0# 23C?>*3C#
>44 ?>A* DEI#0@#>C#*) #2A/ #*) #0A** 1E# 30*AB #>/#>)B) 1#*)AC#*) #3 12)A1B #? * 2*3C# 30*AB #
VZ8YWH#*) #2)>%# ?3 /#C3*1 *@1C/#*3#*) #C314A0# 23C?>*3C#>@C*0#*) #0A** 1E# 30*AB #?13%/#0 D3-#*) #



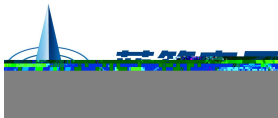
/ Overdischarge Condition

TDL BRCL3130CZF VDL ,
FET VM VM VDD RVMD
IPDN VM VDD
RVMD VM
VDR VCHA ,

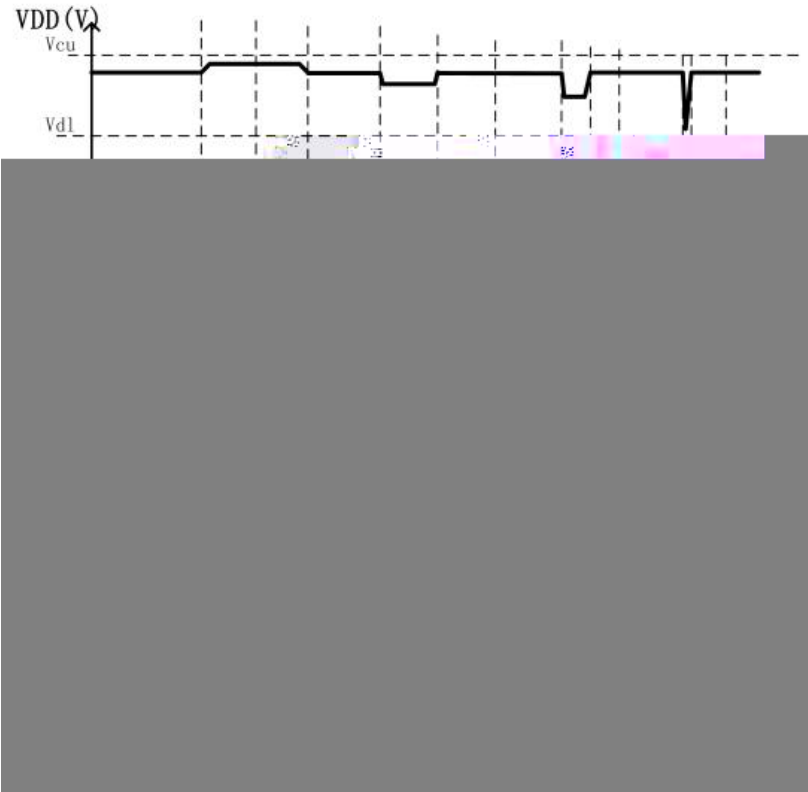
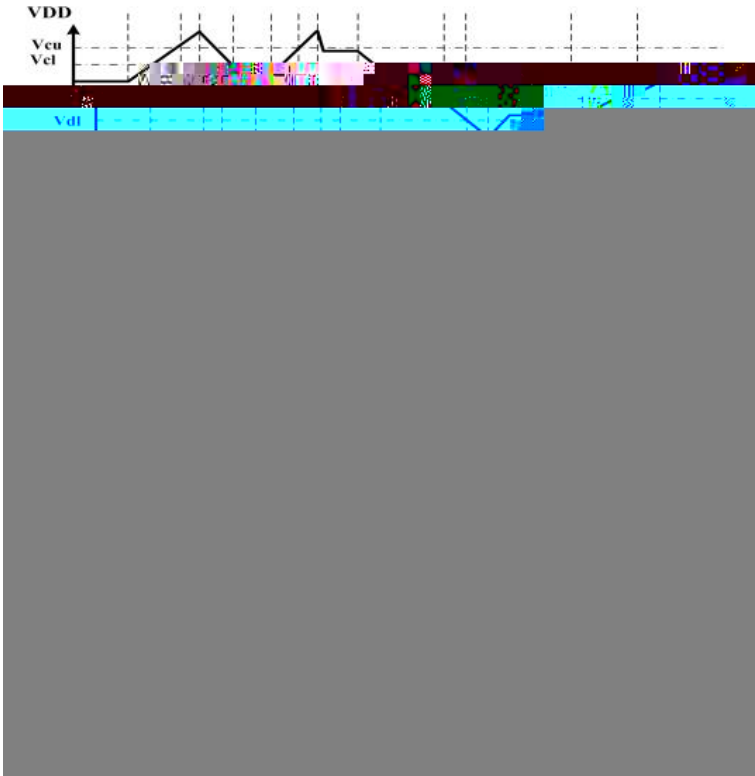
d) C*) #0A** 1E# 3D*AB #?13%/#0 D3-#*) #3 1?>/2)A1B #? * 2*3C# 3D*AB #VZJ9W#?@1>CB#?>/2)A1B>CB#
@C? 1#C314A0#23C?>3C#AC?#*#23C*CB@ /#.31#*) #3 1?>/2)A1B #? * 2*3C#? DAE#*4 #V*J9W#31#D3CB 1I#*) #
7 89: ;: (8<=#*@1C/#*) #?>/2)A1B>CB#23C*13D#H6#3..#AC?#/*3%/#?>/2)A1B>CB!#6)>/#23C?>3C#>/#2A00 ?#
3 1?>/2)A1B #23C?>3C!#".* 1#*) #?>/2)A1B>CB#23C*13D#H6#>/#*@1C ?#3..I#*) #ZF#%>C#>/#%@00 ?#%#OE#*) #
ZFJ#1 />/*31#0 *- C#ZF#AC?#ZJJ#>C#7 89: ;: (8<= *) #2@11 C*#3.#*) #2)>#>/#1 ?@2 ?#*3#*) #
%3- 1&?3-C#2@11 C*#VQRJKW!#6)>/#23C?>3C#>/#2A00 ?#%3- 1&?3-C#23C?>3C!#6) #ZF#AC?#ZJJ#%>C/#A1 #
/)31* ?#OE#*) # ZFJ#1 />/*31!# #
K3* +#Q.#*) #ZF#%>C# 3D*AB #>/#C3#D //#*)AC#*) #2)A1B 1#? * 2*3C# 3D*AB #VZ8 ["W!# -) C*) #0A** 1E#
@C? 1#3 1?>/2)A1B #23C?>3C#>/#23CC 2* ?#*3#A#2)A1B 1I#*) #3 1?>/2)A1B #23C?>3C#>/#1 D A/ ?#V*) #
?>/2)A1B>CB# 23C*13D# =H6#>/#*@1C ?# 3C#W A/# @/@A!# %13 >? ?#*)A*#*) # 0A** 1E# 3D*AB # 1 A2) /#*) #
3 1?>/2)A1B #1 D A/ # 3D*AB #VZJ W#31#>B) 1I# #
#

/ Overcurrent Condition

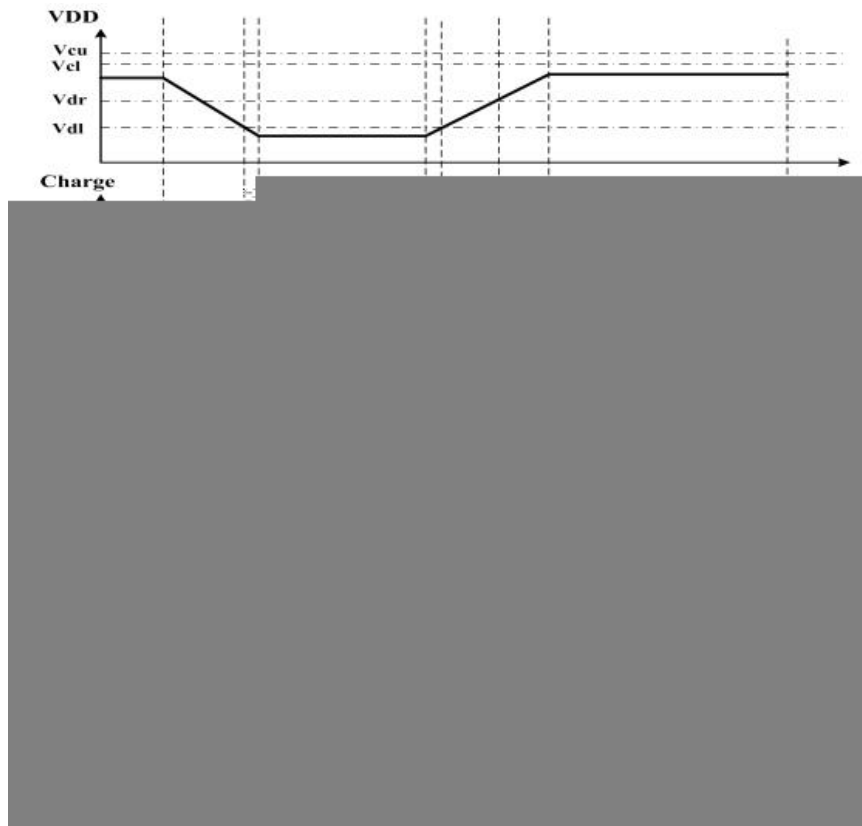
o VM
1 ,BRCL3130CZF FET
2
u



/ Timing Chart



/ Timing Chart

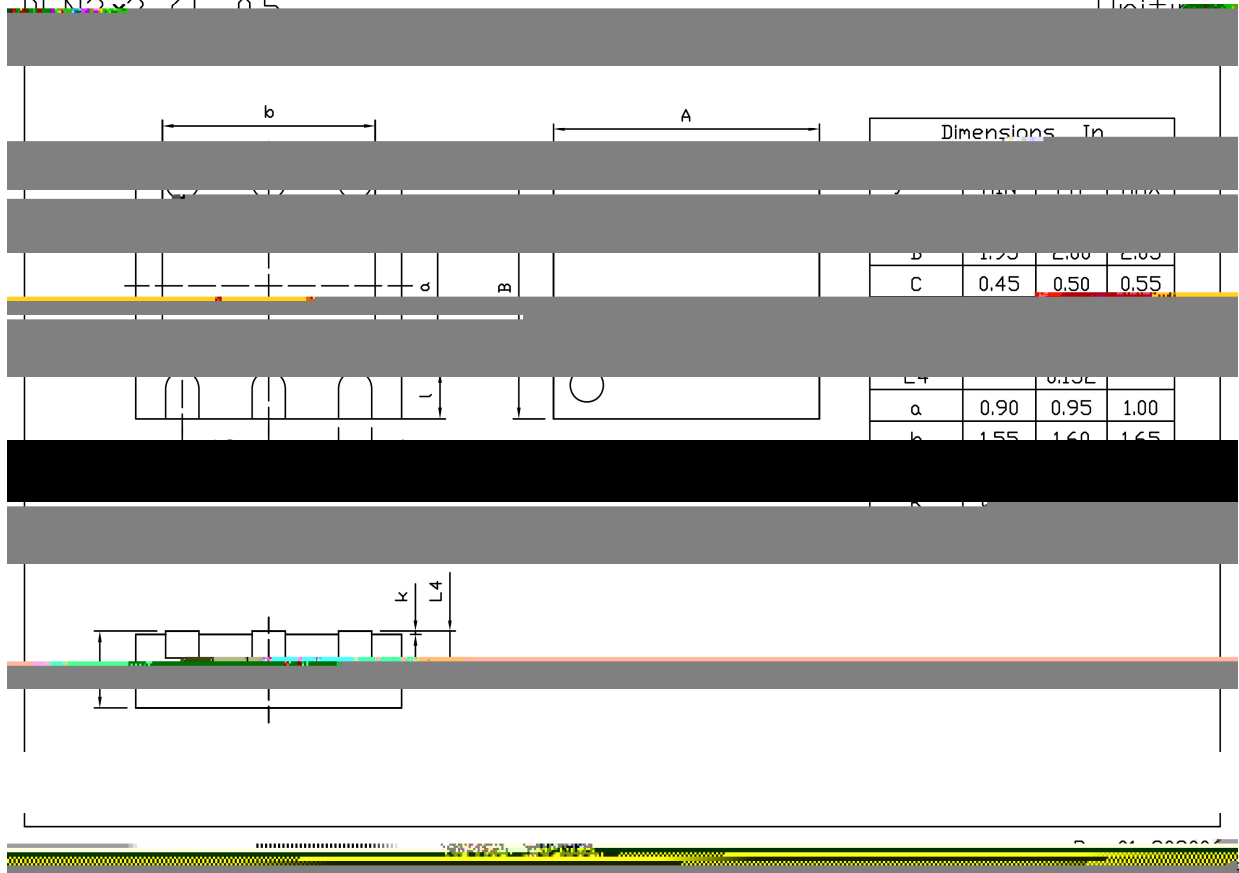


/ Package Dimensions

#

DEN2 v2 71 05

Unit 1



BRCL3130CZF

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BRCL3130CZF



!"#\$ %!&' ('(##### DATA SHEET
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)**0%+,, - AATTI01 2!234## # # # # # # # # # # H1 # # # # 12#,#12