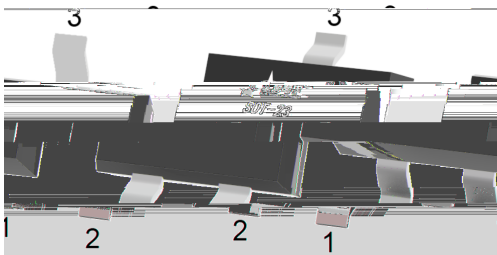
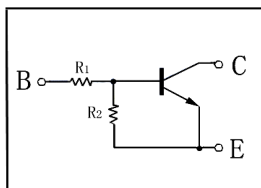


Silicon NPN Digital transistor in a SOT-23 Plastic Package.

With built-in bias resistors, simplify circuit design, reduce a quantity of parts and manufacturing process, Qualified to AEC-Q101 Standards for High Reliability, HF Product.

Switching applications, interface circuit and driver circuit applications, Meet the stringent requirements of automotive applications.

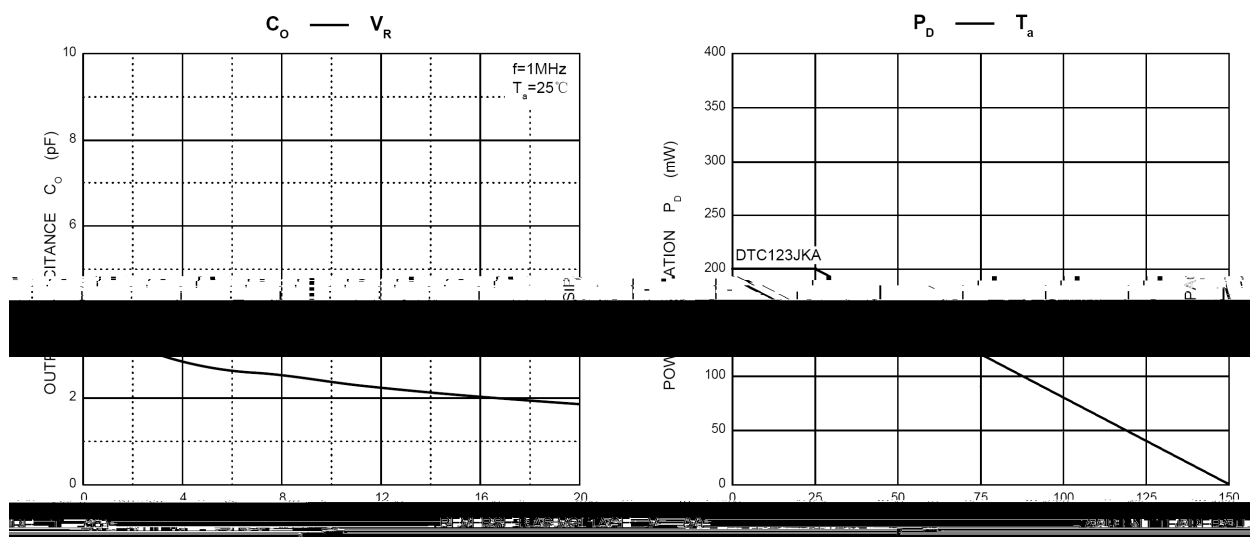
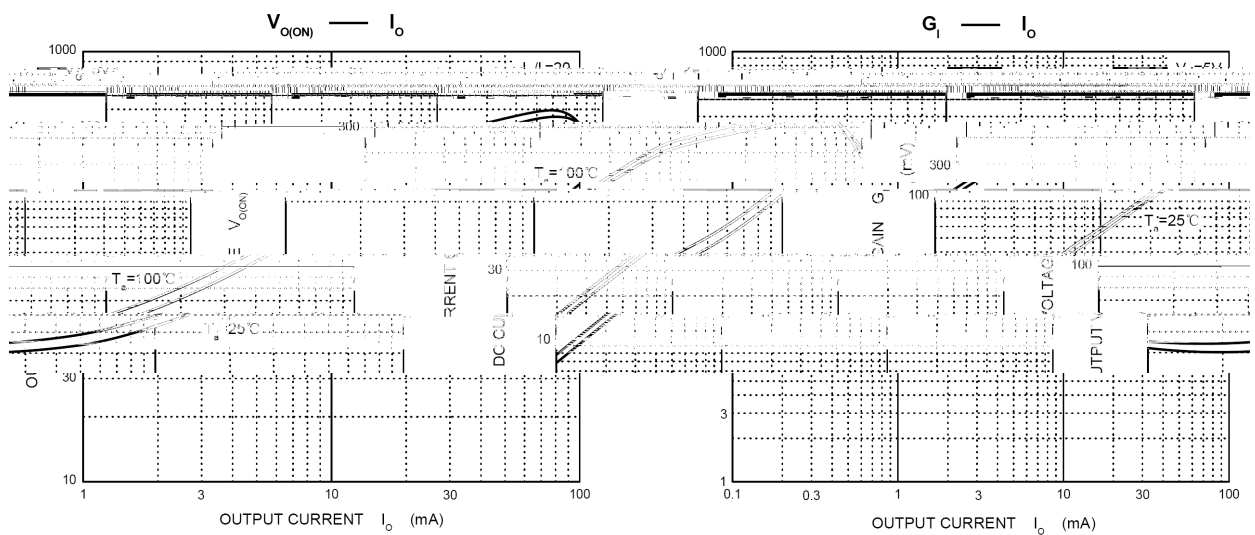
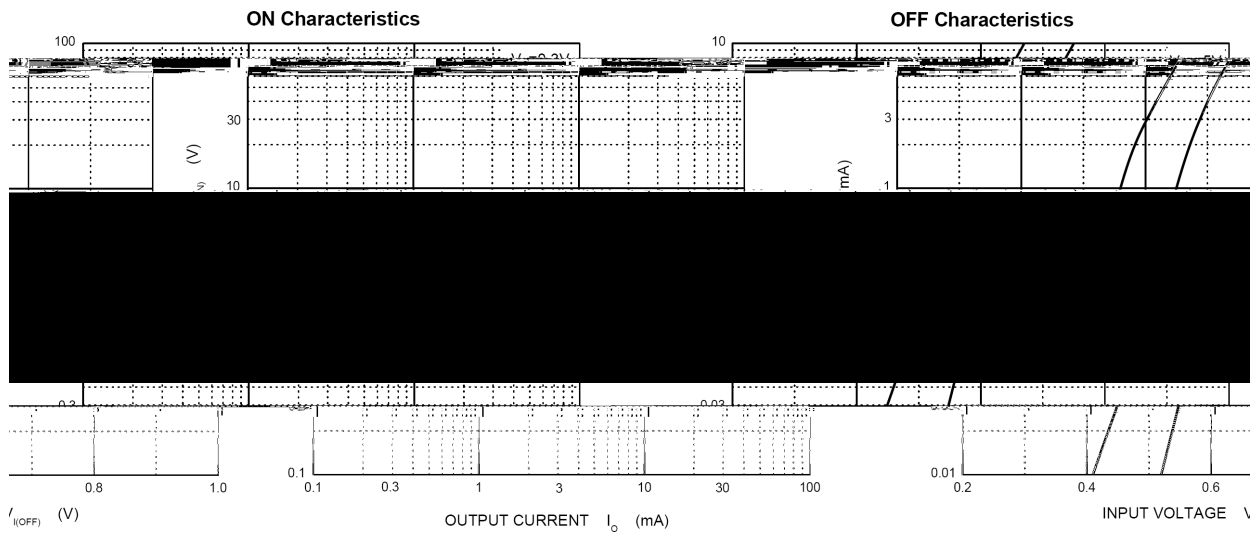


PIN 1 Base PIN 2 Emitter PIN 3 Collector

Marking	QE42
---------	------

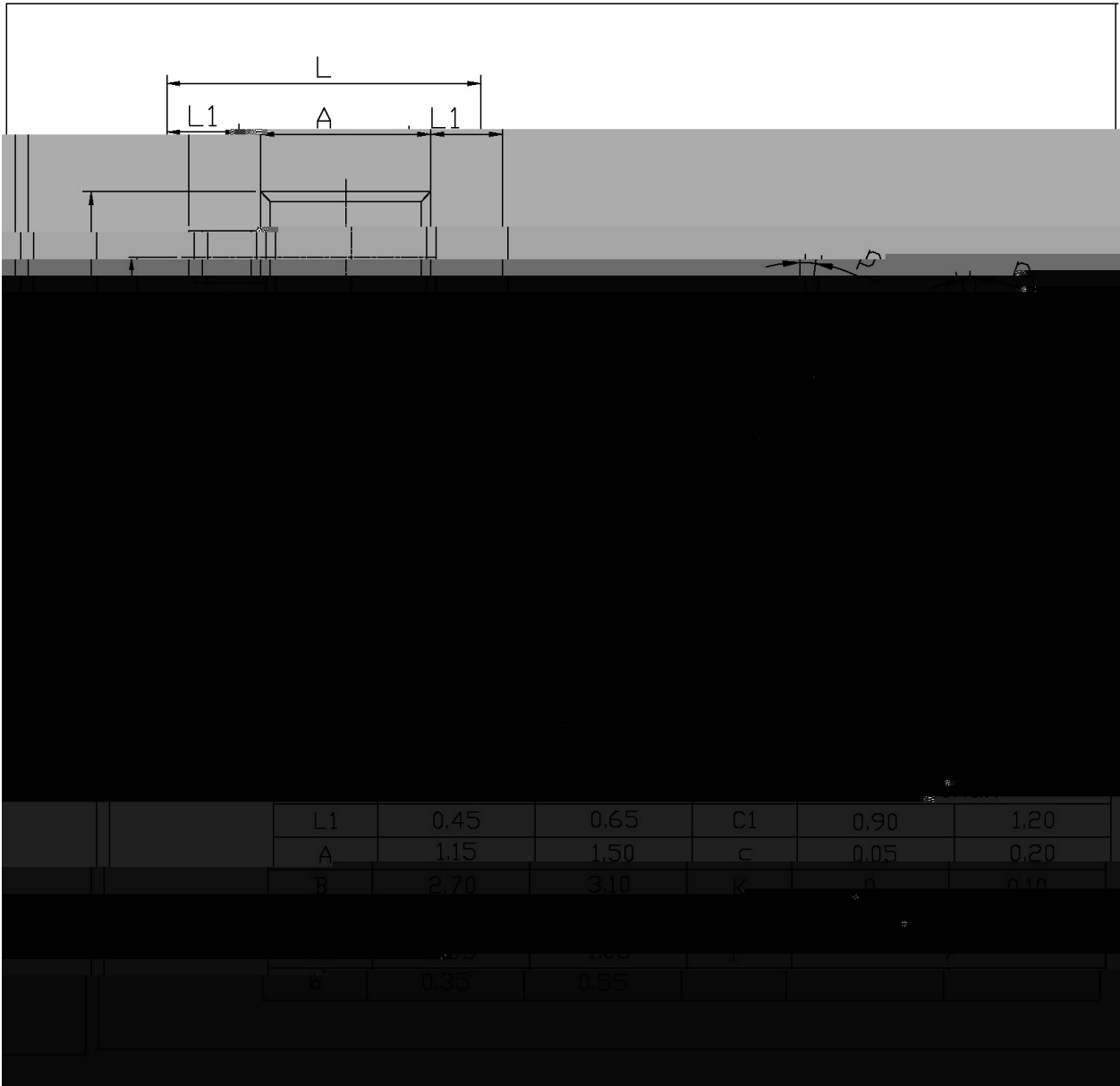
Parameter	Symbol	Rating	Unit
Output Voltage	V_{CC}	50	V
Input Voltage	V_{IN}	12	V
		-5	V
Output Current	I_O	100	mA
	I_c	100	mA
Collector Dissipation	P_C	200	mW
Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	

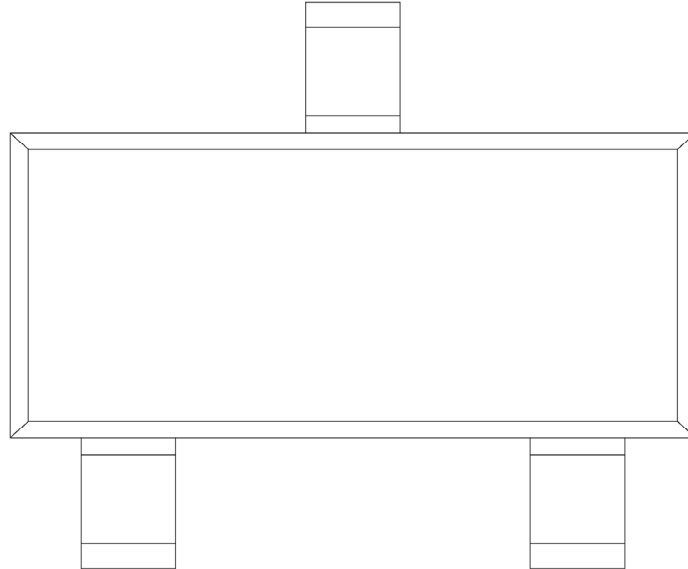
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Voltage	$V_{I(off)}$	$V_{CC}=5.0V$ $I_O=100\mu A$			0.5	V
Input Voltage	$V_{I(on)}$	$V_O=0.3V$ $I_O=5.0mA$	1.1			V
Output Voltage	$V_{O(on)}$	$I_O=5.0mA$ $I_I=0.25mA$		0.1	0.3	V
Input C	I_I	$V_I=5.0V$			3.6	mA
Output Cut-off Current	$I_{O(off)}$	$V_{CC}=50V$ $V_I=0V$			0.5	μA
DC Current Gain	G_I	$V_O=5.0V$ $I_O=10mA$	80			
Transition Frequency	f_T	$V_{CE}=10V$ $I_E=-5.0mA$ $f=100MHz$		250		MHz
Resistance1	R_1		1.54	2.2	2.86	K Ω
Resistance Ratio	R_2/R_1		17	21	26	



SOT-23

单位：mm

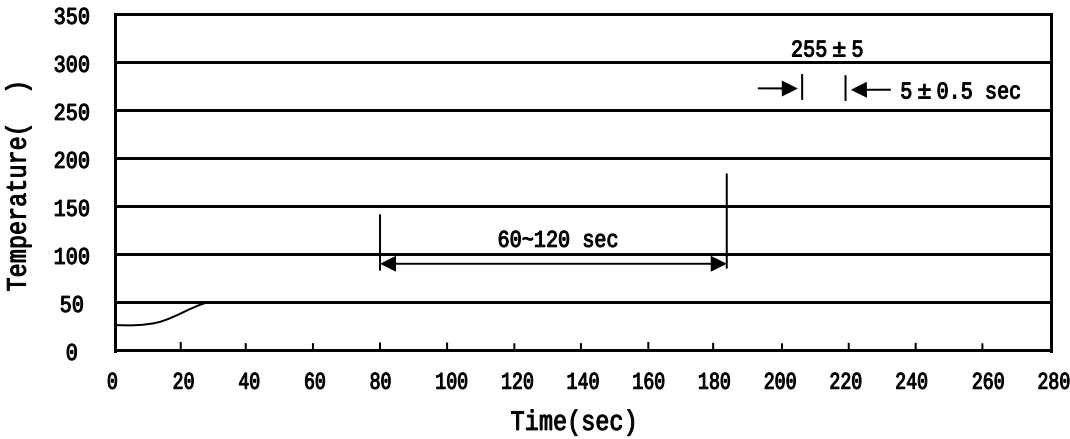




Note:

E42

Temperature Profile for IR Reflow Soldering(Pb-Free)



Note:

- 1

150 200

60 120sec;

1.Preheating:150~200 , Time:60~120sec.
- 2

255 5

5 0.5sec;

2.Peak Temp.:255 5 , Duration:5 0.5sec.
- 3

2 10 /sec.

3.Cooling Speed: 2~10 /sec.

260 5

10 1 sec.

Temp.:260±5

Time:10±1 sec

/ REEL

Package Type	Units					Dimension (unit mm ³)		
SOT-23	3,000	10	30,000	6	180,000	7 ×8	180×120×180	390×385×205