

/ Descriptions

SOT-89 PNP Silicon PNP transistor in a SOT-89 Plastic Package.

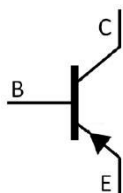
/ Features

， KTC4376
Small flat package, complementary to KTC4376.

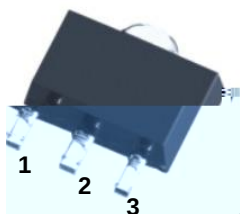
/ Applications

High current application.

/ Equivalent Circuit



/ Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

/ Marking

h_{FE} Classifications Symbol	O	Y
h_{FE} Range	100 200	160 320
Marking	HRO*	HRY*

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-35	V
Collector to Emitter Voltage	V_{CEO}	-30	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-800	mA
Collector Base – Continuous	I_B	-160	mA
Collector Power Dissipation	P_C	500	mW
Collector Power Dissipation*	$*P_C$	1.0	W
Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	

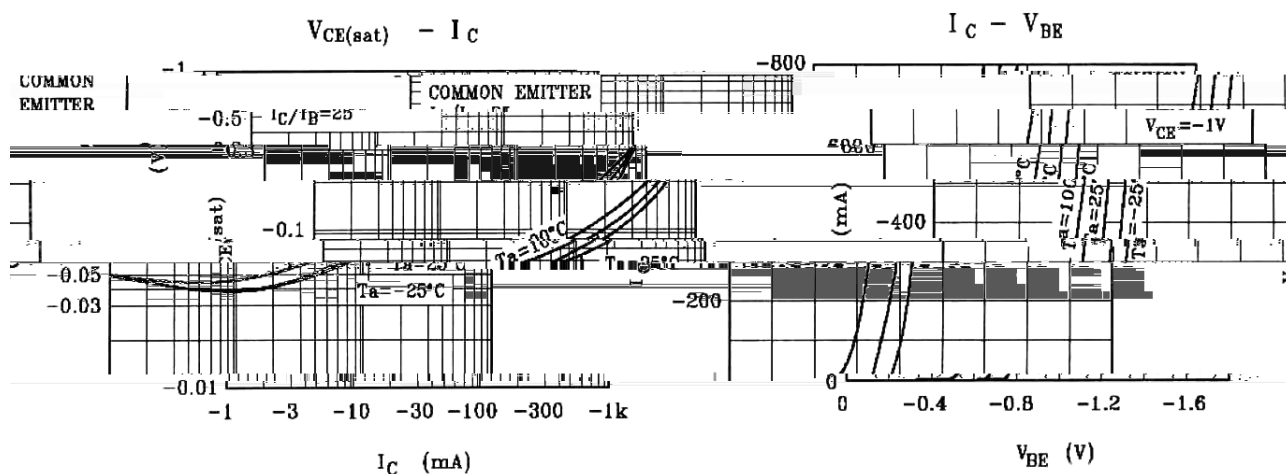
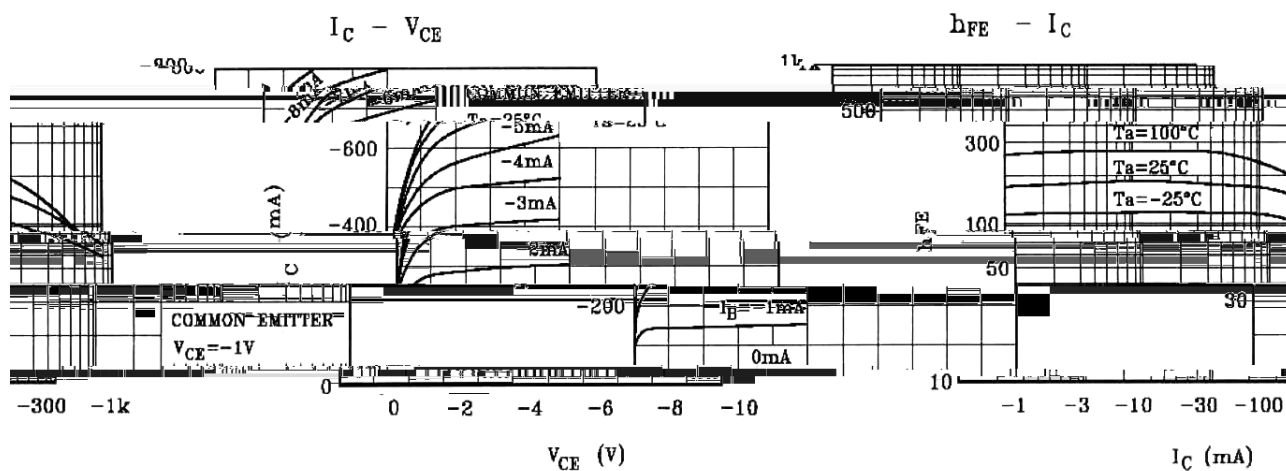
*:mounted on ceramic substrate(250mm²×0.8t)

* 250mm²×0.8t

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector to Emitter Breakdown Voltage	V_{CEO}	$I_C=-10mA$ $I_B=0$	-30			V
Collector Cut-Off Current	I_{CBO}	$V_{CB}=-35V$ $I_E=0$			-0.1	μA
Emitter Base Cut-Off Current	I_{EBO}	$V_{EB}=-5.0V$ $I_C=0$			-0.1	μA

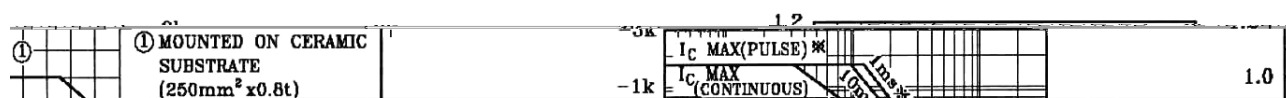
DC Current Gain $h_{FE(1)}$ V_{CE}

/ Electrical Characteristic Curve

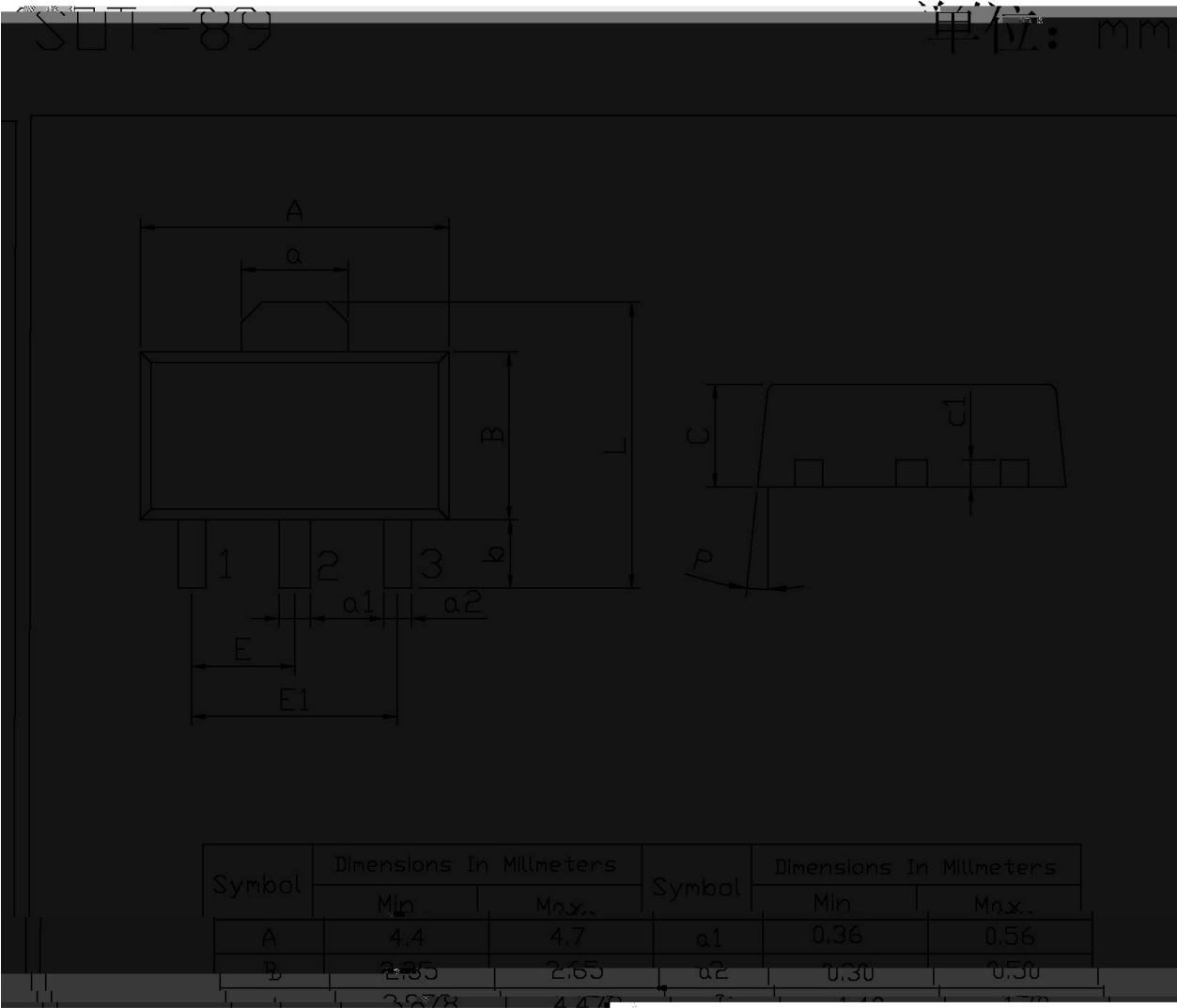


SAFE OPERATING AREA

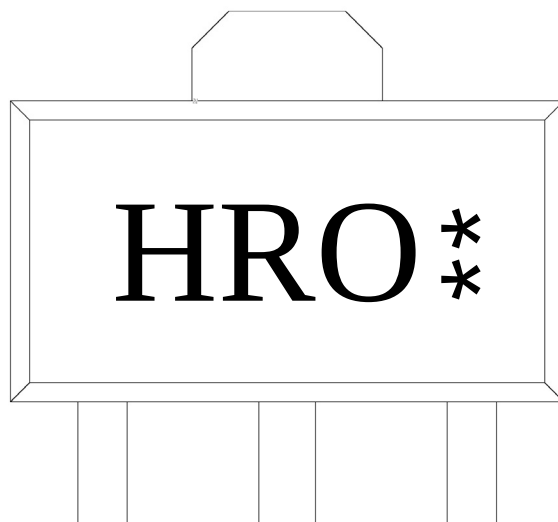
$P_C - T_a$



/ Package Dimensions



/ Marking Instructions



H

R

O: h_{FE}

**

Note:

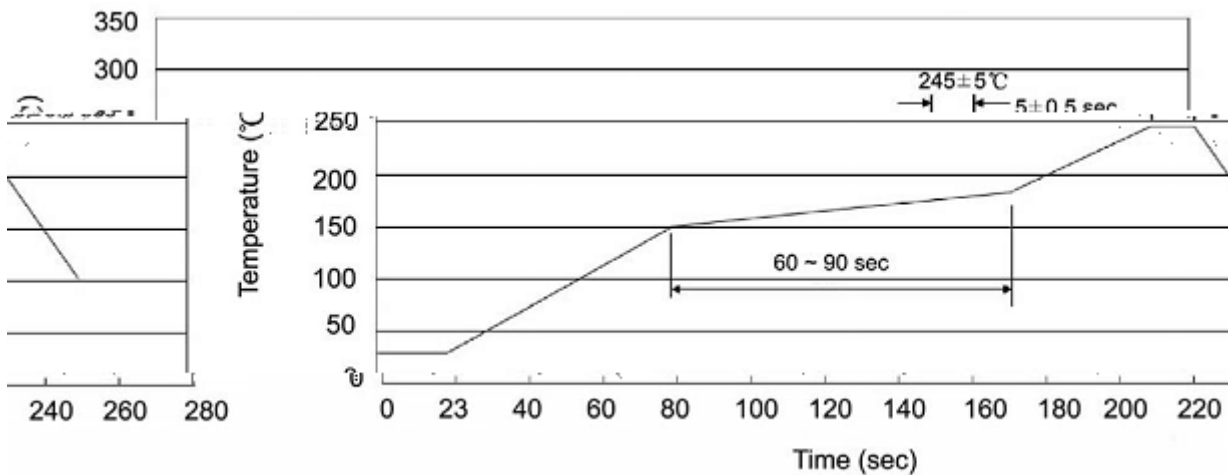
H: Company Code.

R: Product Type.

O: h_{FE} Classifications Symbol

**: Lot No. Code, code change with Lot No.

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Note:

- | | | | | | |
|---|-------|-----|-----------|--------|---|
| 1 | 25 | 150 | 60 | 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 245±5 | | 5±0.5sec; | | 2.Peak Temp.:245±5 , Duration:5±0.5sec. |
| 3 | | 2 | 10 | /sec. | 3. Cooling Speed: 2~10 /sec. |

/ Resistance to Soldering Heat Test Conditions

260±5 10±1 sec. Temp.:260±5 Time:10±1 sec

/ Packaging SPEC.

/ REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm ³)		
	只 卷盘	卷盘 盒	只 盒	盒 箱	只 箱		盒	箱

/ Notices